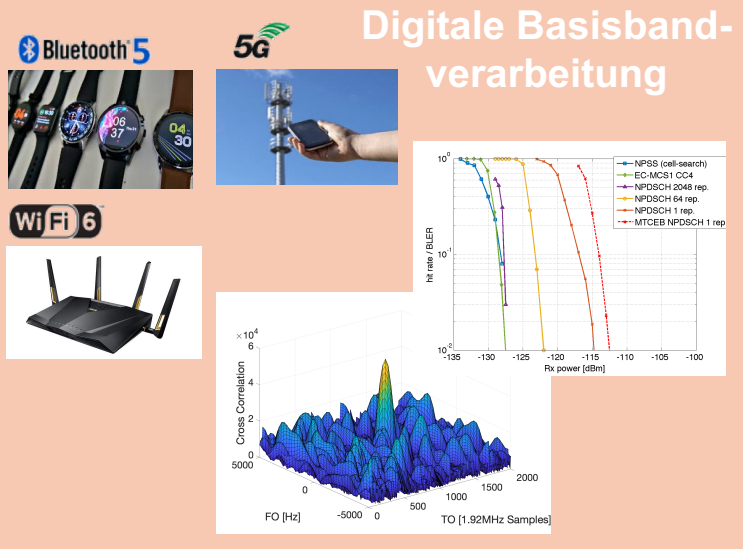


Entwurf Moderner Modern System-on-Chips



HW-SW Co-Design

